

# BIWA SoC development kit

The BIWA SoC development kit enables all the users to start of development activity immediately. The BIWA SoC development kit consists of evaluation board, software utilities and technical manuals.

## Features

### ■ Hardware (Board)

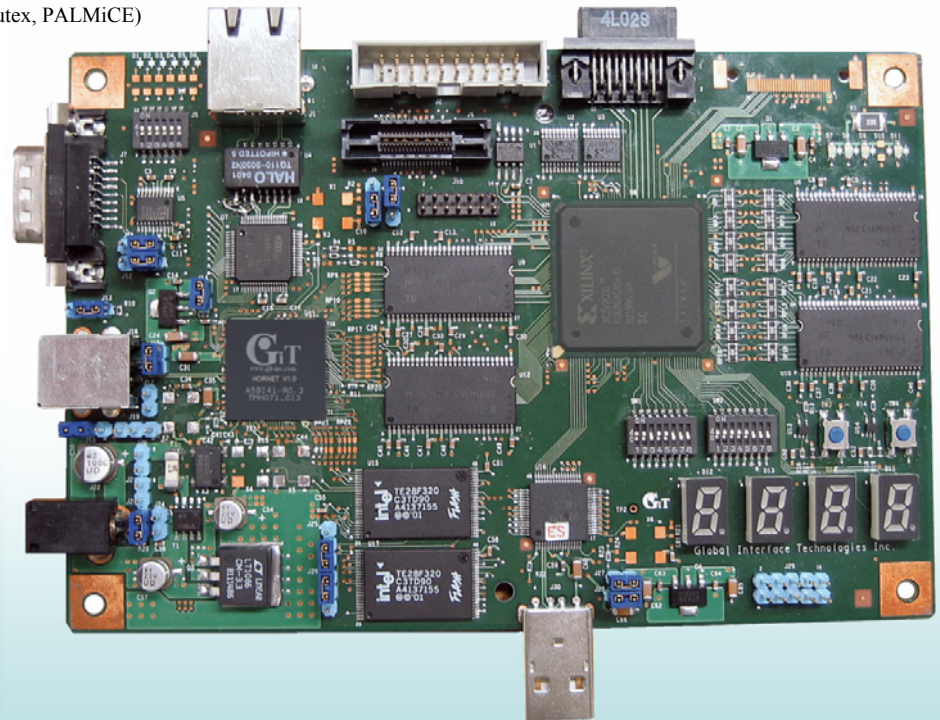
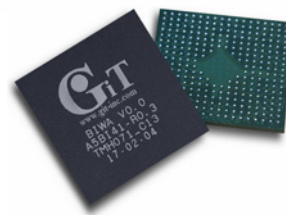
- BIWA SoC; 200Mhz CPU Clock
- Xilinx Virtex-II FPGA with user purpose pins
- 128Mbytes of onboard SDRAM memory
- Two 4Mbytes of onboard Flash memory
- Two 10/100 base-T Ethernet channel ports
- One USB client port (USB 1.1 compliant)
- One GIT UWB interface port
- One RS232C channel port
- CPU Debugger Interface (JTAG)
- FPGA debugger interface (Computex, PALMiCE)

### ■ Software

- The *BIWA SoC development kit* software consists of royalty-free uCLinux distribution including Linux 2.4 port, toolchain (gcc, etc), plus sample codes and documentations.

### ■ Debugger

- Debugger for BIWA SoC is currently available through Sophia Systems Co.Ltd and Lauterbach.



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